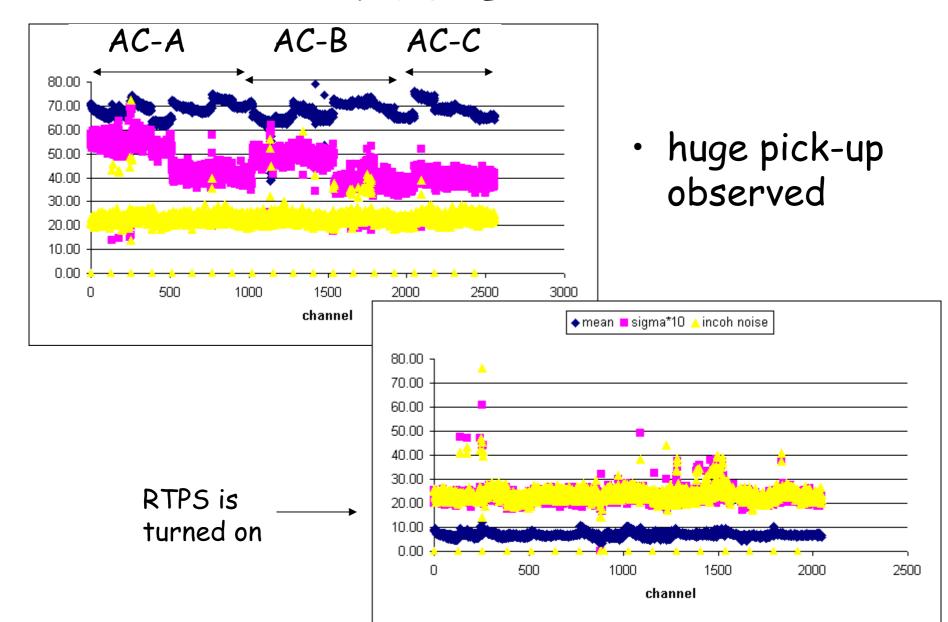
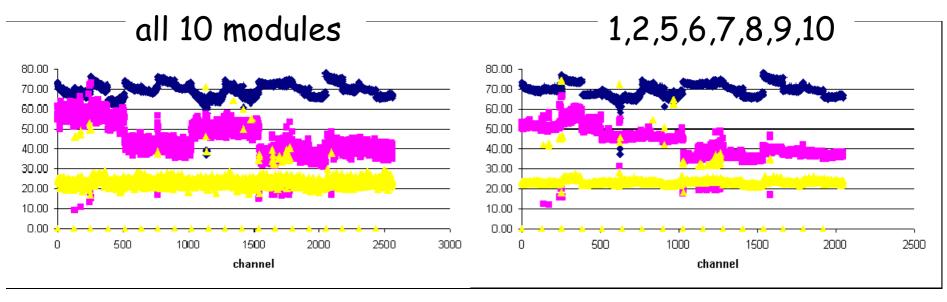
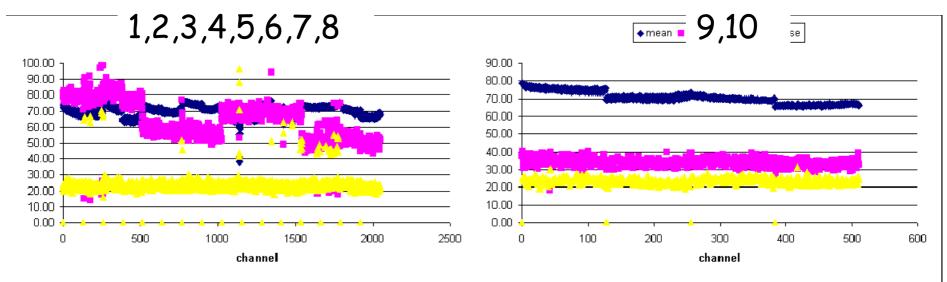


Noise Level

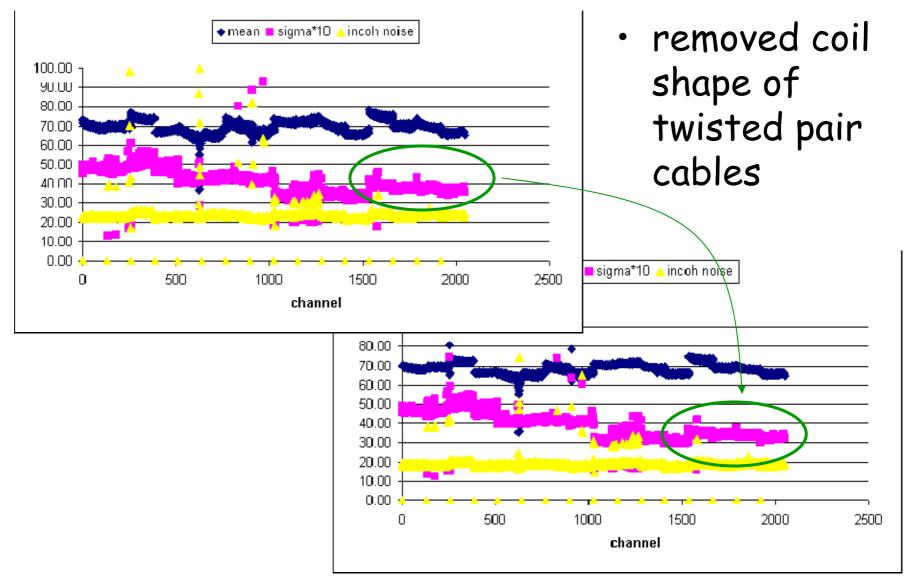


Module or AC dependence?





Inductance between JC and hybrid?

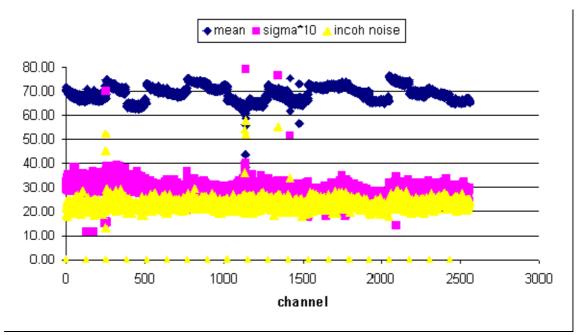


Strange behavior

- Depending on the jumper configurations for bias in interface board, the bias voltage cannot be set properly - drift gradually and reaches to zero eventually
- Potential difference observed among the common(!) ground of AC

After AC replacement

- AC-A: remains the same
- AC-B: new AC with the clock amplitude regulations
- AC-C: use the one in AC-B in the old configuration



Summary

- We don't understand the source of the pick-up noise
- It has gone after replacing the AC's doesn't make sense logically
 - bad connections somewhere?
 - potential difference also seems to disappear
- The current 10+2 channels can be used for A-layer testing